	Application No.	Applicant(s)	
•			
Notice of Allowability	10/630,757	JOSHI, MAYUR	
Notice of Anowability	Examiner	Art Unit	,
	Khai M. Nguyen	2819	And
The MAILING DATE of this communication appearance All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not include will be mailed in due	ed course. <b>THIS</b>
1. This communication is responsive to <u>07-31-2003</u> .			•
2. The allowed claim(s) is/are <u>1-44</u> .			
3. $\boxtimes$ The drawings filed on <u>16 October 2003</u> are accepted by the	e Examiner.		
4. ☐ Acknowledgment is made of a claim for foreign priority una) ☐ All b) ☐ Some* c) ☐ None of the:  1. ☐ Certified copies of the priority documents have 2. ☐ Certified copies of the priority documents have 3. ☐ Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:  Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.  5. ☐ A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give	been received.  been received in Application No cuments have been received in this  of this communication to file a reply IENT of this application.  itted. Note the attached EXAMINER bes reason(s) why the oath or declaration	national stage applicat complying with the req	uirements .
6. CORRECTED DRAWINGS (as "replacement sheets") mus			
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached			
1) hereto or 2) to Paper No./Mail Date		ASST Ali S	
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amenament / Comment or in the C	onice action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t			back) of
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT			lote the
<ul> <li>Attachment(s)</li> <li>1. ☑ Notice of References Cited (PTO-892)</li> <li>2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 09-30-2003</li> <li>4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material</li> </ul>	5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Dat 7. ☐ Examiner's Amendr 8. ☑ Examiner's Stateme 9. ☐ Other	(PTO-413), e nent/Comment	·

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## **DETAILED ACTION**

## Allowable Subject Matter

- 1. Claims 1-44 are allowed.
- 2. The following is an examiner's statement of reasons for allowance: the best prior art (see the cited references) fails to teach or fairly suggest the claimed invention of claims 1-44.

As for claims 1-17 and 23-25, none of the prior art teaches/discloses a priority encoder that includes, among other unique features/components, the first circuitry and the second circuitry as set forth in claims 1-17, and 23-25.

As for claims 18-22, the prior art does not teach/suggest a priority encoding circuit that includes the static circuitry and dynamic circuitry of the claimed invention.

As for claim 26, the prior art does not discloses a circuit that comprises the lower level priority encoding circuits and the upper level priority encoding circuitry as set for in the claim.

As for claims 27-32, the best prior art (see the cited references) also does not disclose or suggest a method of performing priority encoding that includes, among others steps, in response to the subset signals, providing the priority signals, each priority signal indicating whether a respective input signal is asserted and has priority, at most one of the priority signal being asserted at a time, and each priority signal being provided in response to a non-redundant group of the subset signals.

As for claims 33-38 and 40, the prior art fails to teach/disclose an integrated circuit or a system that includes, among other components, a priority encoder, in

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response to input signals, providing priority signals indicating an input signal that is asserted and has priority, wherein the priority encoder comprises the first circuitry and the second circuitry as set for the above claims.

As for claim 39, the prior art does not teach/suggest a system that includes a priority encoder having a static circuitry and a dynamic circuitry of the claimed invention.

As for claims 41-44, none of the prior art disclose/teach or suggest a router which includes a priority encoding circuit that response to match signals, providing signals indicating a match signal that is asserted and has priority, the priority encoder asserting at most one of the priority signals at a time, wherein the priority encoder including the first tree-like/static circuitry and second tree-like/dynamic circuitry as set forth in the above claims. For the reasons above, claims 1-44 are allowed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Contact Information

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khai M. Nguyen whose telephone number is 571-272-1809. The examiner can normally be reached on 8:00 to 4:30 M-F.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael J Tokar can be reached on 571-272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

KN July 27, 2004

> Michael Tokar Supervisory Patent Examiner Technology Center 2800

Muhan J. Tokan